

APPROVAL

The project report on "Design of 8 bit ALU using Verilog" submitted by KABITA PATEL, Roll No. 19409910 of the Department of Pure and Applied Physics, Central University Bilaspur has been accepted as satisfactory for the partial fulfillment of the requirement for the degree of Masters of Science (M.Sc.) in Electronics (Hons) (4th semester) and approved as to its style and contents. The project report has been approved by the following members of the project defense committee.

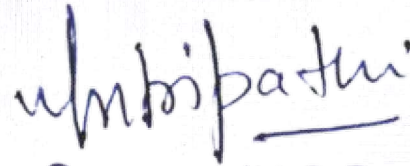
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शुद्ध एवं अनुप्रयुक्त भौतिकी विभाग

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